

Shri Vaishnav Vidyapeeth Vishwavidyalaya													
Choice Based Credit System (CBCS)													
Bachelor of Technology (Electrical Engineering)													
III-SEMESTER (W.E.F. July 2017)													
Sn	Subject Code	Subject Name	Course Code	Teaching			Examination Scheme					Total Marks	Total Credits
				L	T	P	Theory			Practical			
							End Sem University Exam	Two Term Exam	Teachers Assessment*	End Sem University Exam	Teachers Assessment*		
1	BTEE301	Circuit Theory		3	1	2	60	20	20	30	20	150	5
2	BTEE302	Electrical Measurement and Instrumentation		3	0	2	60	20	20	30	20	150	4
3	BTEE303	Analog Electronics		3	1	2	60	20	20	30	20	150	5
4	BTEE305	Electrical Engineering Materials		3	0	0	60	20	20	0	0	100	3
5	BTCS403	Data structure and Algorithm		3	1	2	60	20	20	30	20	150	5
6	ML301	Environmental and Energy Studies		4	0	0	60	20	20	0	0	100	4
Total				19	3	8	360	120	120	120	80	800	26


Chairperson
Board of Studies
 Shri Vaishnav Vidyapeeth Vishwavidyalaya
 Indore


Joint Registrar
 Shri Vaishnav Vidyapeeth
 Vishwavidyalaya, Indore